

**AMENDMENTS TO THE CLAIMS**

Presented below is a complete set of claims with current status indicators.

1. – 19. (canceled)

20. (previously presented) An output circuit for use in an implantable cardiac device comprising:

- an output adapted for connection across a load;
- a charging circuit;
- a first capacitor coupled between the charging circuit and the output;
- a second capacitor switchably coupled across the first capacitor and the output;
- a pulse-width modulation circuit that generates a pulse-width modulation control signal corresponding to a desired waveform; and
- an H-bridge including a first leg and a second leg, each leg including a pulse-width modulation control device configured to receive the pulse-width modulation control signal, and alternately switch between a closed state and an open state in response to the control signal, wherein:
  - when the control device is in a closed state, the first capacitor is coupled across the second capacitor and the output; and
  - when the control device is in an open state, the first capacitor is decoupled across the second capacitor and the output, and the second capacitor is coupled across the output.

21. (previously presented) The output circuit of claim 20 further comprising a comparison circuit that compares the desired output waveform to a timing waveform and provides a control signal to the pulse-width modulation circuit to define the pulse-width modulation control signal.

22. (previously presented) The output circuit of claim 20 wherein the H-bridge comprises a plurality of legs, each leg including a pulse-width modulation control device.

23. (original) The output circuit of claim 22 wherein each leg further includes a polarity control device.

24. (previously presented) The output circuit of claim 23 wherein the H-bridge comprises first, second, and third legs and wherein, when the polarity control device of the first leg controls the polarity, the output voltage modulating devices of the second and third legs are configured to independently alternately couple and decouple the first capacitor across the output.

25. (previously presented) The output circuit of claim 22 wherein the second capacitor is arranged to receive current from the first capacitor when the control device is closed and the first capacitor is coupled across the output and to supply current to the output when the control device is open and the first capacitor is decoupled across the output.

26. (previously presented) The output circuit of claim 25 wherein the second capacitor is a non-polar capacitor.